

# Exam Ba 52700

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READY.

... am 08.09

PC IBQ BK AC AR YR SF NVMDDIZC  
 ADE5 E031 07 0D FF 10 70 ...

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CE16 AD 01 03 LDA #0301 BASIC-Warmstart-Vektor
CE17 07 0E CMP #CE schon verbogen?
CE18 F0 53 BEQ #CE36 ja, RTS
CE19 70 SEI IRQ sperren
CE20 20 8A FF JSR #FFFA I/O initialisieren, Vektoren
CE21 AD 00 03 LDA #0300 Warmstart-Vektor setzen
CE22 0D 07 CE STA #CE07 retten? Selbstmodi-
CE23 AD 03 DF STA #CF35 V fikation.
CE24 AD 01 03 LDA #0301 Hi-Byte
CE25 0D 05 CE STA #CE05 "
CE26 0D 05 CE STA #CE05 "
CE27 AD 25 03 LDA #0325 Output-Vektor Low
CE28 0D 79 CE STA #CE79 } retten
CE29 0D 4E CF STA #CF4E }
CE30 AD 07 03 LDA #0307 O-Vektor High
CE31 0E 74 CE STA #CE74 }
CE32 0E 4F CF STA #CF4F }
CE33 AD 18 03 LDA #0318 NMI-Vektor
CE34 0D 4B CE STA #CE4B }
CE35 0D 03 CF STA #CF03 }
CE36 AD 19 03 LDA #0319 High-Byte
CE37 0E 48 CE STA #CE48 :
CE38 0D 04 CF STA #CF04 :
CE39 09 70 LDA #475 }
CE40 0D 00 03 STA #0300 } Warmstart = $CE75
CE41 A7 0E LDA #CE }
CE42 0E 01 03 STA #0301 }
CE43 A7 00 LDA #400 }
CE44 0D 39 CE STA #CE39 }
CE45 0D 3A CE STA #CE3A }
CE46 A7 3C LDA #33C }
CE47 40 CE LDY #CE EMERGENCY-BASIC VIA
CE48 1C 1C 0E JMP #AB1E (C) N. HEUSLER 12.8.81
CE49 00 RTS et. Ausgabe
CE50 00 BRK
CE51 37 77 BRK
CE52 00 BRK
CE53 00 BRK
CE54 00 BRK
CE55 00 BRK
CE56 00 BRK
CE57 00 BRK
CE58 00 BRK
CE59 00 BRK
CE60 00 BRK
CE61 00 BRK
CE62 00 BRK
CE63 00 BRK
CE64 00 BRK
CE65 00 BRK
CE66 00 BRK
CE67 00 BRK
CE68 00 BRK
CE69 00 BRK
CE70 00 BRK
CE71 00 BRK
CE72 00 BRK
CE73 00 BRK
CE74 00 BRK
CE75 00 BRK
CE76 00 BRK
CE77 00 BRK
CE78 00 BRK
CE79 00 BRK
CE80 00 BRK
CE81 00 BRK
CE82 00 BRK
CE83 00 BRK
CE84 00 BRK
CE85 00 BRK
CE86 00 BRK
CE87 00 BRK
CE88 00 BRK
CE89 00 BRK
CE90 00 BRK
CE91 00 BRK
CE92 00 BRK
CE93 00 BRK
CE94 00 BRK
CE95 00 BRK
CE96 00 BRK
CE97 00 BRK
CE98 00 BRK
CE99 00 BRK

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CE5B 45 57 EOR #55
CE5C 53 70 EOR #70
CE5D 4E 45 32 JMP #5245
CE5E 20 2A 72 JSR #5231
CE61 22 37 31 ROL #3137
CE64 05 0D 12 ORA #120D
CE67 13 77 EOR #77
CE68 12 77 EOR #77
CE69 05 1B 58 EOR #521D
CE6C 15 57 EOR #57
CE6E 54 77 EOR #77
CE6F 4F 77 EOR #77
CE70 52 77 EOR #77
CE71 55 72 EOR #72
CE72 0D 00 PNA #00
CE73 0D 00 BHA #00
CE74 0D 00 STA #CE37 }
CE75 0D 39 CE STA #CE39 }
CE76 A7 07 LDA #407 Output-
CE77 0D 26 03 STA #0326 Vektor
CE78 07 CE LDA #0CE
CE79 0D 27 03 STA #0327
CE80 03 63 siehe CE44 }
CE81 1C 00 JMP #0000 }
CE82 08 00 PNA #00 } Status Output
CE83 78 SEI } IRQ verhindern
CE84 1C PNA Akku retten
CE85 A9 9B LDA #19B } $CE9B
CE86 0D 10 03 STA #0310 }
CE87 A7 0E LDA #40E } NMI-Vektor setzen
CE88 0D 19 03 STA #0319 }
CE89 08 68 PLA Akku holen
CE90 28 00 FLP status holen
CE91 4C 00 00 JMP #0000 Output alt
CE92 4B PNA } neuer NMI
CE93 0A TXA }
CE94 1E PNA } Register retten
CE95 70 PNA }
CE96 1D PNA }
CE97 1D PNA }
CE98 AD 39 CE LDA #CE39 (=0?)
CE99 F0 00 BEQ #BEAD
CEA0 03 63 PLA
CEA1 45 TAY }
CEA2 4E PLA } Register holen
CEA3 4A TAX }
CEA4 28 00 PLA }
CEA5 4C 00 00 JMP #0000 NMI-alt
CEA6 20 F0 CF JSR #CEFF0 auf keine Taste warten
CEA7 09 DF CMP #DF (Comma)?
CEA8 03 4E BNE #DE45 nein
CEA9 2E 39 CE LDC #CE39 =1
CEAB 20 F0 CF JSR #CEFF0 warten
CEAC 09 FT CMP #4FF prüfen
CEAD 00 F7 BNE #CEB7 nein
CEAE 76 01 LDA #01
CEAF 0D 30 CE STA #CE30 Spst 1 retten
CEB0 AD 00 DD LDA #DD00 CIA
CEB1 0D 3B CE STA #CE3B retten
CEB2 A7 30 LDA #430 auf RAM
CEB3 0E 01 STA #01 umschalten
CEB4 A7 00 LDA #400 Quelle
CEB5 A0 00 LDY #400 Ziel (RAM unter 10)

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Test

Text

# EmBa 52700

CE01	A2	28	LDX	#428	\$28 Seiten	CF61	20	E0	CF	JSR	\$CFF0	warten
CE03	20	D5	CF	JSR	\$CFD5	\$0000-\$2800 nach \$0000	CF64	E7	FF	CMP	#1FF	Commo?
CE06	A9	37	LDA	#437	auf ROM	CF6C	D0	F9	BNE	\$CF61	nein	
CE08	05	01	STA	\$01		CF69	A9	30	LDA	#430		
CE0A	A0	00	LDY	#400		CF6A	05	01	STA	\$01	RAM	
CE0C	E7	00	DB	LDA	\$D800, Y	CF6D	A9	D2	LDA	#4D2	D200	
CE0F	99	00	F8	STA	\$FB00, Y	CF6E	A0	02	LDY	#402	→ D200	
CE12	B9	00	D9	LDA	\$D900, Y	CF70	A2	26	LDX	#426	\$26 Seiten kopieren	
CE15	99	00	F9	STA	\$F900, Y	CF72	20	D5	CF	JSR	\$CFD5	
CE18	B9	00	DA	LDA	\$DA00, Y	CF75	A0	00	LDY	#400		
CE1B	99	00	FA	STA	\$FA00, Y	CF77	A9	30	LDA	#430	RAM (?)	
CE1E	B9	00	DB	LDA	\$DB00, Y	CF79	05	01	STA	\$01		
CE1F	99	00	FB	STA	\$FB00, Y	CF7D	E7	00	D1	LDA	\$D100, Y	
CE24	B9	00	DO	LDA	\$D000, Y	CF7E	99	00	01	STA	\$0100, Y	
CE27	99	00	EC	STA	\$EB00, Y	CF81	B9	00	DO	LDA	\$D000, Y	
CE2A	08		INX			CF84	99	00	00	STA	\$0000, Y	
CE2B	DO	DF	BNE	\$CEDD	LOOP	CF87	A9	35	LDA	#435	RAM+1/0	
CE2D	BA		TSX			CF89	05	01	STA	\$01		
CE2E	0E	37	CE	STX	\$CE37	Stapelzeiger retten	CF8B	B9	00	FB	LDA	\$FB00, Y
CE31	A2	FF	LDX	#4FF	Stack	CF8E	99	00	DE	STA	\$DB00, Y	
CE33	7A		TXS		initialisieren	CF91	B9	00	F9	LDA	\$F900, Y	
CE34	20	04	FF	JSR	\$FFB4	CIAs initial.	CF94	99	00	D9	STA	\$D900, Y
CE37	20	0A	FF	JSR	\$FFBA	1/0 initial.	CF97	B9	00	FA	LDA	\$FA00, Y
CE3A	A2	00	LDX	#400		CF9A	99	00	DA	STA	\$DA00, Y	
CE3C	A0	28	LDY	#428	\$2800	CF9D	B9	00	FB	LDA	\$FB00, Y	
CE3E	18		CLD		als BASIC-Ende setzen	CF9E	99	00	DB	STA	\$DB00, Y	
CE3F	20	97	FF	JSR	\$FF97	VIC-Reset	CFA3	08		INX		
CE42	20	01	FF	JSR	\$FFB1		CFA4	D0	D1	BNE	\$CF77	
CE45	A9	37	LDA	#437		CFA6	B9	00	FD	LDA	\$FD00, Y	
CE47	0D	26	03	STA	\$0326	Output-Vektor auf \$CF37	CFA9	97	00	DO	STA	\$D000, Y
CE4A	A9	0F	LDA	#40F		CFAC	08		INX			
CE4C	0D	27	03	STA	\$0327	IRG zulassen	CFAD	C0	2F	CPY	#42F	
CE4F	08		CLI			CFAF	D0	F5	BNE	\$CFA4	LOOP	
CE50	20	00	00	JMP	(\$A000)	BASIC	CFB1	AD	11	DO	LDA	\$D011
CE53	4B		PHA		retten	CE54	27	7F	AND	#47F	?	
CE54	A9	00	LDA	#400	Warmstart im EmBa	CE56	0D	11	DO	STA	\$D011	
CE56	0D	3A	CE	STA	\$CE3A	CE59	AD	3B	CE	LDA	\$CE3B	
CE59	A9	37	LDA	#437		CE5C	0D	00	DD	STA	\$DD00	
CE5B	0D	26	03	STA	\$0326	Output \$CF37	CE5F	AD	3B	CE	LDA	\$CE3B
CE5E	A9	CF	LDA	#4CF		CE62	05	01	STA	\$01	setzen	
CE60	0D	27	03	STA	\$0327		CE64	AE	37	CE	LDX	\$CE37
CE63	68		PLA		holen alter	CE67	7A		TXS		setzen	
CE64	4E		JMP	\$0000	Warmstart-Output-neu im EmBa	CE68	A9	00	LDA	#400	auf 0	
CE67	4B		PHA		retten	CE6A	0D	3F	CE	STA	\$CE3F	
CE68	A9	50	LDA	#450		CE6D	68		PLA		Register holen	
CE6A	0D	18	03	STA	\$0318	NMI \$CF50	CE6E	A0		TAY		
CE6D	A9	CF	LDA	#4CF		CE6F	4B		PLA		Ende	
CE6E	0D	19	03	STA	\$0319		CE70	AA		TAX		
CE6F	A9	23	LDA	#423		CE71	68		PLA		← alter NMI	
CE72	0D	00	03	STA	\$0300	Waste \$CF23	CE73	4E	04	01	JMP	\$0000
CE75	A9	CF	LDA	#4CF		CE76	0D	0F	CF	STA	\$CFDE	
CE78	0D	01	03	STA	\$0301		CE78	0C	E2	CF	STY	\$CFE2
CE7A	68		PLA		holen	CE7D	A0	00	LDY	#400		
CE7C	4C		JMP	\$0000	Output-alt	CE7E	B9	00	DO	LDA	\$D000, Y	
CE7E	7B		SEI		IRG setzen	CE80	99	00	DO	STA	\$D000, Y	
CE80	4B		PHA		retten	CE83	08		INX			
CE83	0D	3A	CE	LDA	\$CE3A		CE84	D0	F7	BNE	\$CFDD	
CE86	D0	7A	BNE	\$CED1		CE86	EE	DF	CF	TNC	\$CFDE	
CE89	EE	3A	CE	INC	\$CE3A	= 0 = 1	CE89	EE	E2	CF	INC	\$CFE2
CE8A	20	F0	CF	JSR	\$CFF0	auf Änderung warten	CE8C	DA		DEX		
CE8D	07	DF	CMP	#4DF	Keine Taste?	CE8D	D0	EE	BNE	\$CFDD	LOOP	
CE8F	D3	70	BNE	\$CFD1	nein						zählen k=1-1 nicht Null, LOOP	

