

# MIST LOAD V2

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CB6C A9 78 LDA #78
CB6E 8D 3D 03 STA $0330
CB71 A9 CD LDA #CD
CB73 8D 31 03 STA $0331
CB76 60 RTE

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Einsprung  
Load-Routine  
= \$CD78

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CB77 JSR v CD09 LDA #BA
CB79 20 00 CD21 JSR $ED0C LISTEN
CB7C A9 6F LDA #6F

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CB7E 20 B9 ED JSR $EDB9 Sekundärade: Befehlskanal
CB81 A9 4D LDA #4D "M"
CB83 20 DD ED JSR $EDDD senden
CB86 A9 2D LDA #2D "-"

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CB88 JSR v CD00 JSR $EDDD senden A/K senden
CB8B 8A CD07 TXA
CB8C 4C DD ED JMP $EDDD senden

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CB8F 20 42 4C 4F 43 4B 53 20 BLOCKS
CB97 46 52 45 45 2E 00 44 45 FREE. SDF
CB9F 4C 53 45 51 50 52 47 55 LSEQPRBL
CBA7 53 52 52 45 4C A9 20 20 SRREL

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CBAC A9 20 LDA #20
CBAE 20 12 CD JSR $CD12
CBB1 20 0A CD JSR $CDOA
CBB4 A9 12 LDA #12
CBB6 20 E4 CE JSR $CEE4
CBB9 20 02 CD JSR $CD02
CBBC A9 00 LDA #00
CBBE 20 8A CE JSR $CE8A
CBC1 A9 00 LDA #00
CBC3 AA TAX
CBC4 20 CD BD JSR $BDCC
CBC7 20 58 CC JSR $CCF8
CBCA A9 12 LDA #12
CBCD 20 D2 FF JSR $FFD2
CBCF 20 F5 CD JSR $CCF5
CBD2 A9 00 LDA #00
CBD4 B5 F9 STA $F9
CBD6 A9 EB LDA #EB
CBD8 B5 FA STA $FA
CBDA A0 90 LDY #90
CBDC 20 FD CC JSR $CCFD
CBDF 20 D2 FF JSR $FFD2
CBE2 CB INY
CBE3 C0 A0 CPY #A0
CBE5 D0 F5 BNE $CBDC
CBE7 20 F5 CC JSR $CCF5
CBEA 20 F8 CC JSR $CCF8
CBED A0 A2 LDY #A2
CBEE 20 FD CC JSR $CCFD
CBF2 20 D2 FF JSR $FFD2
CBF5 CB INY
CBF6 C0 A7 CPY #A7
CBF8 D0 F5 BNE $CBEE
CBFA 20 D7 AA JSR $AAD7
CBFD A0 00 LDY #00
CBFF 20 FD CC JSR $CCFD

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CC02 D0 03 BNE $CC07
CC04 4C B2 CC JMP $CCB2
CC07 CB INY
CC08 20 FD CC JSR $CCFD
CC0B 18 CLC
CC0C 69 EB ADC #EB
CC0E B5 FA STA $FA
CC10 A9 00 LDA #00
CC12 B5 F9 STA $F9
CC14 A0 02 LDY #02
CC16 20 FD CC JSR $CCFD
CC19 D0 03 BNE $CC1E
CC1B 4C A5 CC JMP $CCA5
CC1E 98 TYA
CC1F 4B PHA
CC20 18 CLC
CC21 A9 1C ADC #1C
CC23 A9 TAY
CC24 20 FD CC JSR $CCFD
CC27 AA TAX
CC28 CB INY
CC29 20 FD CC JSR $CCFD
CC2C 20 CD BD JSR $BDCC
CC2F A2 00 LDX #00
CC31 B0 00 01 LDA $0100,X
CC34 F0 03 BEQ $CC39
CC36 EB INX
CC37 D0 F8 BNE $CC31
CC39 BA TXA
CC3A 38 SEC
CC3B E9 06 SBC #06
CC3D 49 FF EOR #FF
CC3F AA TAX
CC40 20 EE CC JSR $CCEE
CC43 20 F5 CC JSR $CCF5
CC46 68 PLA
CC47 4B PHA
CC48 18 CLC
CC49 69 03 ADC #03
CC4B AB TAY
CC4C A2 10 LDX #10
CC4E 20 FD CC JSR $CCFD
CC51 CB INY
CC52 C9 A0 CMP #A0
CC54 F0 06 BEQ $CC5C
CC56 20 D2 FF JSR $FFD2
CC59 CA DEX
CC5A D0 F2 BNE $CC4E
CC5C 20 F5 CC JSR $CCF5
CC5F E0 00 CPX #00
CC61 F0 03 BEQ $CC66
CC63 20 EE CC JSR $CCEE
CC66 68 PLA
CC67 AB TAY
CC68 20 FD CC JSR $CCFD
CC6B 30 03 BMI $CC70
CC6D A9 2A LDA #2A
CC6F 20 A9 20 BIT $20A9
CC72 20 D2 FF JSR $FFD2
CC75 20 FD CC JSR $CCFD
CC78 29 0F AND #0F

```

```

,CC7A 85 F7 STA #F7
,CC7C 0A ASL
,CC7D 18 CLC
,CC7E 85 F7 ADC #F7
,CC80 AA TAX
,CC81 BD 9D CB LDA #CB9D,X
,CC84 20 D2 FF JSR #FFD2
,CC87 BD 9E CB LDA #CB9E,X
,CC8A 20 D2 FF JSR #FFD2
,CC8D BD 9F CB LDA #CB9F,X
,CC90 20 D2 FF JSR #FFD2
,CC93 20 FD CC JSR #CCFD
,CC96 29 40 AND #140
,CC9B F0 05 BEQ #CC9F
,CC9A A9 3C LDA #13C
,CC9C 20 D2 FF JSR #FFD2
,CC9F 20 D0 FB JSR #FBD0
,CCA2 20 D7 AA JSR #AAD7
,CCA5 98 TYA
,CCA6 18 CLC
,CCA7 69 20 ADC #120
,CCA9 90 03 BCC #CCAE
,CCAB 4C FD CB JMP #CBFD
,CCAE A8 TAY
,CCAF 4C 16 CC JMP #CC16
,CCB2 A9 04 LDA #104
,CCB4 85 F9 STA #F9
,CCB6 A9 EB LDA #1EB
,CCB8 85 FA STA #FA
,CCBA A2 23 LDX #123
,CCBC A0 00 LDY #100
,CCBE 84 FF STY #FF
,CCC0 98 TYA
,CCC1 20 0A CD JSR #CD0A
,CCC4 18 CLC
,CCC5 71 F9 ADC (#F9),Y
,CCC7 90 02 BCC #CCCB
,CCC9 EA FF INC #FF
,CCCB C8 INY
,CCCE C8 INY
,CCCD C8 INY
,CCCE C8 INY
,CCCF 0A DEX
,CCD0 D0 F2 BNE #CCC4
,CCD2 A0 44 LDY #144
,CCD4 38 SEC
,CCD5 F1 F9 SBC (#F9),Y
,CCD7 B0 02 BCS #CCDB
,CCD9 C6 FF DEC #FF
,CCDB 20 02 CD JSR #CD02
,CCDE AA TAX
,CCDF A5 FF LDA #FF
,CCE1 20 CD BD JSR #BDCD
,CCE4 A9 8F LDA #18F
,CCE6 A0 CB LDY #1CB
,CCEB 20 1E AB JSR #AB1E
,CCEB 4C 79 CE JMP #CE79
,CCEE 20 FB CC JSR #CCFB
,CCF1 CA DEX
,CCF2 D0 FA BNE #CCEE
,CCF4 60 RTS
,CCF5 A9 22 LDA #122

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,CCF7 20 A9 20 BIT #20A9
,CCFA 4C D2 FF JMP #FFD2
,CCFD 20 0A CD JSR #CD0A
,CD00 B1 F9 LDA (#F9),Y
,CD02 48 PHA
,CD03 A9 37 LDA #137
,CD05 85 01 STA #01
,CD07 58 CLI
,CD08 60 PLA
,CD09 60 RTS
,CD0A 48 PHA
,CD0B A9 35 LDA #135
,CD0D 78 4 SETI
,CD0E 85 01 STA #01
,CD10 68 PLA
,CD11 60 RTS
,CD12 48 V,CE04 B STA #CB2A
,CD15 A9 14 LDA #114
,CD17 85 FE STA #FE
,CD19 A9 CB LDA #1CB
,CD1B 85 FF STA #FF
,CD1D A0 00 LDY #100
,CD1F A2 57 LDX #157 "W"
,CD21 20 77 CB JSR #CB77 "M-W" senden
,CD24 98 TYA = 0 bzw. Index
,CD25 A2 06 LDX #106 $0600 oder $0623, etc.
,CD27 20 8B CB JSR #CB8B senden
,CD2A A9 23 LDA #123 128 Bytes senden
,CD2C AA TAX
,CD2D 20 DD ED JSR #EDDD sende
,CD30 B1 FE LDA (#FE),Y Byte holen
,CD32 20 DD ED JSR #EDDD Byte senden
,CD35 C8 INY
,CD36 CA DEX
,CD37 D0 F7 BNE #CD30 alle 128
,CD39 20 FE ED JSR #EDFE UNLISTEN
,CD3C C0 60 CPY #160
,CD3E 90 DF BCC #CD1F 160 Bytes senden
,CD40 A2 45 LDX #145 "E"
,CD42 20 77 CB JSR #CB77 "M-E" senden
,CD45 A9 00 LDA #100
,CD47 A2 06 LDX #106 $0600
,CD49 20 8B CB JSR #CB8B austischen
,CD4C 20 FE ED JSR #EDFE UNLISTEN
,CD4F 78 SETI
,CD50 2C 00 DD BIT #DD00 warten, bis
,CD53 70 FB BVS #CD50 Impuls = 0
,CD55 20 C7 CE JSR #CEC7 Daten = 1, Impuls = 0
,CD58 A9 6F LDA #16F
,CD5A 85 FE STA #FE
,CD5C A9 CB LDA #1CB
,CD5E 85 FF STA #FF
,CD60 A2 03 LDX #103
,CD62 A0 00 LDY #100 Index = 0
,CD64 BA TXA
,CD65 48 PHA
,CD66 B1 FE LDA (#FE),Y Byte holen
,CD68 20 BA CE JSR #CEBA senden
,CD6B 60 PLA
,CD6C AA TAX
,CD6D C8 INY Index erhöhen

```

\$3C = % 00111100

CD8E D0 F4 BNE \$CD24 *ganzen Block*  
 CD70 E6 F5 INC \$FF *High-Byte erhöhen*  
 CD72 DA DEX *schon alle 8?*  
 CD73 D0 EF BNE \$CD64 *nein*  
 CD75 B6 02 STX \$02 = 0  
 CD77 60 RTS  
 CD78 B5 93 STA \$93  
 CD7A B6 F7 STX \$F7  
 CD7C B4 FB STY \$FB  
 CD7E C9 00 CMP #00 *Load/Verify*  
 CD90 F0 05 BEQ \$CD87 *Locat*  
 CD82 A6 E7 LDX \$F7  
 CD84 4C A5 F4 JMP \$F4A5  
 CD87 A6 BA LDX \$BA  
 CD89 E0 08 CPX #08  
 CD8B 90 F5 BCC \$CD82 *Gerät kleiner*  
 CD8D A6 B7 LDX \$B7  
 CD8F F0 F1 BEQ \$CD82  
 CD91 A9 3C LDA #3C  
 CD93 BD 02 DD STA \$DD02 *Datenrichtungsregister: VIC auf Eingangs schalten*  
 CD96 A0 00 LDY #00  
 CD98 B1 BB LDA (\$BB), Y *Filename*  
 CD9A C9 24 CMP #24 *1. Zeichen = "?"*  
 CD9C D0 03 BNE \$CDA1 *nein*  
 CD9E 4C AC CB JMP \$CBAC *2. Directory*  
 CDA1 AD 15 D0 LDA \$D015  
 CDA4 8D 75 CE STA \$CE75 *Sprite-Enable setzen*  
 CDA7 A9 00 LDA #00  
 CDA9 8D 15 D0 STA \$D015 *Sprites aus*  
 CDAC 20 AF F5 JSR \$F5AF *"SEARCHING FOR (name)"*  
 CDAF 20 33 F3 JSR \$F333 *CLRCH*  
 CDB2 A5 B9 LDA \$B9 *Sekundäradresse*  
 CDB4 8D 18 CE STA \$CE18 *markieren*  
 CDB7 A9 40 LDA #40  
 CDB9 85 B9 STA \$B9 *= LOAD*  
 CDBB 20 D5 F3 JSR \$F3D5 *IEC-OPEN*  
 CDBE A5 BA LDA \$BA  
 CDC0 20 09 ED JSR \$ED09 *TALK*  
 CDC3 A5 B9 LDA \$B9  
 CDC5 20 C7 ED JSR \$EDC7 *Sekundäradresse*  
 CDC8 20 13 EE JSR \$EE13 *Byte holen*  
 CDCB A5 90 LDA \$90  
 CDCD 4A LSR  
 CDCE 4A LSR  
 CD0F 90 06 BCC \$CD07 *kein Fehler*  
 CDD1 20 B4 DE JSR \$DEB4 *DDR zurücksetzen*  
 CDD4 4C 04 B7 JMP \$F704 *FILE NOT FOUND*  
 CDD7 A2 52 LDX #52  
 CDD9 20 77 CB JSR \$CB77 *"H-R" senden*  
 CDDC A9 18 LDA #18  
 CDDF A2 00 LDY #00  
 CDE0 20 B8 CE JSR \$CEB8 *senden*  
 CDE3 A9 02 LDA #02  
 CDE5 20 DD ED JSR \$EDDD *Bytes lesen*  
 CDE8 20 FE ED JSR \$EDFE *UNLISTEN*  
 CDEB A5 BA LDA \$BA  
 CDED 20 09 ED JSR \$ED09 *TALK*  
 CDF0 A9 6F LDA #6F *Befelskanal*  
 CDF2 20 C7 ED JSR \$EDC7  
 CDF5 20 13 EE JSR \$EE13 *Byte holen (Track)*  
 CDF8 B5 FC STA \$FB  
 CDFA 20 13 EE JSR \$EE13 *Byte holen (Sektor)*

Load-Routine

Load/Verify Locat

Gerät kleiner

Datenrichtungsregister: VIC auf Eingangs schalten

Filename 1. Zeichen = "?"

nein 2. Directory

Sprite-Enable setzen

Sprites aus

"SEARCHING FOR (name)" CLRCH

Sekundäradresse markieren

= LOAD

IEC-OPEN

TALK

Sekundäradresse Byte holen

kein Fehler

DDR zurücksetzen

FILE NOT FOUND

"H-R" senden

senden

Bytes lesen

UNLISTEN

TALK

Befelskanal

Byte holen (Track)

Byte holen (Sektor)

CD8D B5 FB STA \$FB  
 CDFF 20 EF ED JSR \$EDEF *UNTALK*  
 CE02 A9 2C LDA #2C  
 CE04 20 12 CD JSR \$CD12 *Routine übertragen*  
 CE07 20 D2 F5 JSR \$F5D2 *LOADING*  
 CE0A A9 35 LDA #35 *ROM aus*  
 CE0C 78 SEI *Interrupts aus*  
 CE0D B5 01 STA \$01 *Track*  
 CE0F A5 FE LDA \$FC  
 CE11 20 E4 CE JSR \$CEE4  
 CE14 20 CD CE JSR \$CECD  
 CE17 20 D9 CE JSR \$CED9  
 CE1A A9 FF LDA #FF  
 CE1C F0 09 BEQ \$CE27  
 CE1E B1 F9 LDA (\$F9), Y  
 CE20 C8 INY  
 CE21 B5 F7 STA \$F7  
 CE23 B1 F9 LDA (\$F9), Y  
 CE25 B5 F8 STA \$F8  
 CE27 A9 04 LDA #04  
 CE29 A2 FC LDX #FC  
 CE2B 18 CLC  
 CE2D E5 F9 ADC \$F9  
 CE2E B5 F9 STA \$F9  
 CE30 90 02 BCC \$CE34  
 CE32 E6 FA IND \$FA  
 CE34 A5 FC LDA \$FC  
 CE36 D0 06 BNE \$CE3E  
 CE38 A5 F9 LDA \$FB  
 CE3A 30 SEC  
 CE3B E9 01 SBC #01  
 CE3D 24 BA BIT \$BA  
 CE3F B5 F6 STA \$F6  
 CE41 A0 00 LDY #00  
 CE43 D1 F9 LDA (\$F9), Y  
 CE45 91 F7 STA (\$F7), Y  
 CE47 C8 INY  
 CE48 C4 F6 CPY \$F6  
 CE4A D0 F7 BNE \$CE43  
 CE4C A5 F6 LDA \$F6  
 CE4E 18 CLC  
 CE4F A5 F7 ADC \$F7  
 CE51 B5 F7 STA \$F7  
 CE53 90 02 BCC \$CE57  
 CE55 F6 FB INC \$FB  
 CE57 A5 FC LDA \$FC  
 CE59 F0 0F BEQ \$CE6A  
 CE5B 20 E4 CE JSR \$CEE4  
 CE5E 20 CD CE JSR \$CECD  
 CE61 20 D9 CE JSR \$CED9  
 CE64 C9 02 LDA #02  
 CE66 A2 FE LDX #FE  
 CE68 D0 C1 BNE \$CE6B  
 CE6A A9 00 LDA #00  
 CE6C 20 BA CE JSR \$DEBA  
 CE6F A9 37 LDA #37  
 CE71 B5 01 STA \$01  
 CE73 5B CLI  
 CE74 A9 FF LDA #FF  
 CE76 8D 15 DD STA \$D015  
 CE78 A9 40 LDA #40

zum Verändern von \$DD02:

|        | Daten-ein | Impuls-ein | Daten-aus | Impuls-aus | ATN-aus | RS-232-aus | VIC-Bank | Wert |
|--------|-----------|------------|-----------|------------|---------|------------|----------|------|
| normal | ein       | ein        | auss      | auss       | auss    | auss       | auss     | 43F  |
| hier   | ein       | ein        | auss      | auss       | auss    | auss       | ein      | \$3C |

|                 |               |                      |
|-----------------|---------------|----------------------|
| CE78 05 90      | ORA \$90      |                      |
| CE7D 85 90      | STA \$90      |                      |
| CE7F A6 F7      | LDX \$F7      |                      |
| CE81 A4 F8      | LDY \$F8      |                      |
| CE83 1B         | CLC           |                      |
| CE84 JSR v.CDD1 | LDA #\$3F     | Normalwert in DDR    |
| CE86 8D 02 DD   | STA \$DD02    |                      |
| CE87 60         | RTS           |                      |
| CE8A JSR v.CD88 | BIT \$DD00    | warten bis Input = 1 |
| CE8D 5D F       | BVC \$CEBA    | Byte retten          |
| CE8F 4B         | PHA           |                      |
| CE90 44         | LSR           |                      |
| CE91 4A         | LSR           | low-Byte             |
| CE92 4A         | LSR           | isolieren            |
| CE93 4A         | LSR           |                      |
| CE94 AA         | TAX           | als Index            |
| CE95 AD 12 D0   | LDA \$D012    |                      |
| CE9B C9 32      | CMP #\$32     |                      |
| CE9A 90 06      | BCC \$CEA2    | oberer Rahmen        |
| CE9C 29 07      | AND #\$07     |                      |
| CE9E C9 02      | CMP #\$02     |                      |
| CEA0 F0 F3      | BEQ \$CE95    | kritische Zeile      |
| CEA2 A9 03      | LDA #\$03     | Input = 0            |
| CEA4 8D 00 DD   | STA \$DD00    | Daten = 0            |
| CEA7 8D 33 CF   | LDA \$CF33, X |                      |
| CEAA 8D 00 DD   | STA \$DD00    | senden               |
| CEAD 4A         | LSR           |                      |
| CEAE 4A         | LSR           |                      |
| CEAF 29 F0      | AND #\$F0     |                      |
| CEB1 8D 00 DD   | STA \$DD00    |                      |
| CEB4 6B         | PLA           |                      |
| CEB5 29 0F      | AND #\$0F     | low-Byte             |
| CEB7 AA         | TAX           | als Index            |
| CEB8 8D 33 CF   | LDA \$CF33, X |                      |
| CEBB 8D 00 DD   | STA \$DD00    |                      |
| CEBE 4A         | LSR           |                      |
| CEBF 4A         | LSR           |                      |
| CEC0 29 F0      | AND #\$F0     |                      |
| CEC2 8D 00 DD   | STA \$DD00    |                      |
| CEC5 EA         | NOP           |                      |
| CEC6 EA         | NOP           |                      |

|                 |                       |                      |
|-----------------|-----------------------|----------------------|
| CEC7 JSR v.CD55 | LDA #\$23 = %00100011 |                      |
| CEC9 8D 00 DD   | STA \$DD00            | Daten = 1, Input = 0 |
| CECC 60         | RTS                   |                      |
| CECD A5 FB      | LDA \$FB              |                      |
| CECF 18         | CLC                   |                      |
| CED0 69 EB      | ADC #\$EB             |                      |
| CED2 85 FA      | STA \$FA              |                      |
| CED4 A0 00      | LDY #\$00             |                      |
| CED6 84 F9      | STY \$F9              |                      |
| CED8 60         | RTS                   |                      |
| CED9 B1 F9      | LDA (\$F9), Y         |                      |
| CEDB CB         | INY                   |                      |
| CEDC 85 FC      | STA \$FC              |                      |
| CEDE B1 F9      | LDA (\$F9), Y         |                      |
| CEE0 CB         | INY                   |                      |
| CEE1 85 FB      | STA \$FB              |                      |
| CEE3 60         | RTS                   |                      |

|                 |            |                          |
|-----------------|------------|--------------------------|
| CEE4 JSR v.CE11 | CMP #02    | Track schon im Speicher? |
| CEE6 F0 13      | BEQ \$CEFB | ja RTS                   |
| CEE8 85 02      | STA #02    |                          |
| CEEA 20 BA CE   | JSR \$CEBA | Tracks. senden           |

Bit  
0 Data in  
1 Data out  
2 Clock in  
3 Clock out

|                 |                |                                |
|-----------------|----------------|--------------------------------|
| CEED 2D FC      | JSR \$CEFC     | Byte holen (Sektorur.)         |
| CEFD 30 09      | BMI \$CEFB     | RTS, Ende                      |
| CEF2 1B         | CLC            |                                |
| CEF3 69 EB      | ADC #\$EB      | + gleicher Offset              |
| CEF5 20 43 CE   | JSR \$CF43     | Sektor holen                   |
| CEFB 4C ED CE   | JMP \$CEED     |                                |
| CEFA 30         | RTS            |                                |
| CEFC JSR v.CEED | BIT \$DD00     | Byte holen                     |
| CEFF 50 FB      | BVC \$CEFC     |                                |
| CF01 AD 12 D0   | LDA \$D012     |                                |
| CF04 C9 32      | CMP #\$32      |                                |
| CF06 90 06      | BCC \$CF0E     | Rahmen                         |
| CF08 29 07      | AND #\$07      |                                |
| CF0A C9 02      | CMP #\$02      |                                |
| CF0C F0 F3      | BEQ \$CF01     | kritische Zeile                |
| CF0E A9 03      | LDA #\$03      |                                |
| CF10 8D 00 DD   | STA \$DD00     |                                |
| CF13 4B         | PHA            |                                |
| CF14 6B         | PLA            |                                |
| CF15 24 CE      | 3 BIT \$EA     |                                |
| CF17 AD 00 DD   | 4 LDA \$DD00   |                                |
| CF1A 4A         | 2 LSR          |                                |
| CF1B 4A         | 2 LSR          |                                |
| CF1C 4D 00 DD   | 4 EOR \$DD00   |                                |
| CF1F 4A         | 2 LSR          |                                |
| CF20 4A         | 2 LSR          |                                |
| CF21 4D 00 DD   | 4 EOR \$DD00   |                                |
| CF24 4A         | 2 LSR          |                                |
| CF25 4A         | 2 LSR          |                                |
| CF26 4D 00 DD   | 4 EOR \$DD00   |                                |
| CF29 4B         | 3 PHA          |                                |
| CF2A A9 23      | 2 LDA #\$23    |                                |
| CF2C 8D 00 DD   | 4 STA \$DD00   |                                |
| CF2F 6B         | PLA            |                                |
| CF30 49 03      | EOR #\$03      |                                |
| CF32 60         | RTS            |                                |
| CF33 00         | BRK            |                                |
| CF34 80         | ???            |                                |
| CF35 20 A0 40   | JSR \$40A0     |                                |
| CF3B C0 60      | CPY #\$60      |                                |
| CF3A E0 10      | CRX #\$10      |                                |
| CF3C 90 30      | BCC \$CF6E     |                                |
| CF3E B0 50      | BCS \$CF90     |                                |
| CF40 D0 70      | BNE \$CEB2     |                                |
| CF42 F0         | <del>RTS</del> | Sektor holen                   |
| CF43 3 A0 00    | LDY #800       | \$FD/E = Ziel des Sektors      |
| CF45 JSR v.CEFS | STA \$FE       |                                |
| CF47 84 FD      | STY \$FD       |                                |
| CF49 84 FE      | STY \$FE       |                                |
| CF4B 8B         | DEY = \$FF     |                                |
| CF4C 2C 00 DD   | BIT \$DD00     |                                |
| CF4F 50 FB      | BVC \$CF4C     |                                |
| CF51 AD 12 D0   | LDA \$D012     | auf Zeile vor "8 Zeile" warten |
| CF54 29 07      | AND #\$07      |                                |
| CF56 C9 02      | CMP #\$02      |                                |
| CF58 D0 F7      | BNE \$CF51     |                                |
| CF5A AD 12 D0   | LDA \$D012     | auf "8. Zeile" warten          |
| CF5D 29 07      | AND #\$07      |                                |
| CF5F C9 03      | CMP #\$03      |                                |
| CF61 D0 F7      | BNE \$CF5A     |                                |
| CF63 A2 02      | LDX #\$02      |                                |

|               |                            |
|---------------|----------------------------|
| CF65 EA       | NOP                        |
| CF66 BD 00 DD | STA \$DD00 = 3             |
| CF69 2C 00 DD | BIT \$DD00                 |
| CF6C 70 FB    | BVS \$CF69                 |
| CF6E 2C 00 DD | BIT \$DD00                 |
| CF71 70 02    | BVS \$CF75                 |
| CF73 EA       | NOP                        |
| CF74 EA       | NOP                        |
| CF75 EA       | NOP                        |
| CF76 AD 00 DD | LDA \$DD00 2 Bit holen     |
| CF79 4A       | LSR                        |
| CF7A 4A       | LSR                        |
| CF7B 4D 00 DD | EOR \$DD00 2 Bit holen     |
| CF7E 49 30    | EOR #\$30                  |
| CF80 4A       | LSR                        |
| CF81 4A       | LSR                        |
| CF82 C8       | INY                        |
| CF83 EA       | NOP                        |
| CF84 EA       | NOP                        |
| CF85 4D 00 DD | EOR \$DD00 2 Bit holen     |
| CF88 4A       | LSR                        |
| CF89 4A       | LSR                        |
| CF8A 4D 00 DD | EOR \$DD00 2 Bit holen     |
| CF8D 91 FD    | STA (\$FD), Y Byte ablegen |
| CF8F 45 FF    | EOR \$FF                   |
| CF91 85 FF    | STA \$FF Prüfsumme         |
| CF93 AD 00 DD | LDA \$DD00 2 Bit holen     |
| CF96 4A       | LSR                        |
| CF97 4A       | LSR                        |
| CF98 4D 00 DD | EOR \$DD00 2 Bit holen     |
| CF9B 49 30    | EOR #\$30                  |
| CF9D EA       | NOP                        |
| CF9E C8       | INY                        |
| CF9F 4A       | LSR                        |
| CFA0 4A       | LSR                        |
| CFA1 4D 00 DD | EOR \$DD00 2 Bit holen     |
| CFA4 4A       | LSR                        |
| CFA5 4A       | LSR                        |
| CFA6 4D 00 DD | EOR \$DD00 2 Bit holen     |
| CFA9 91 FD    | STA (\$FD), Y Byte ablegen |
| CFAB 45 FF    | EOR \$FF                   |
| CFAD 85 FF    | STA \$FF Prüfsumme         |
| CFAF AD 00 DD | LDA \$DD00 2 Bit           |
| CFB2 4A       | LSR                        |
| CFB3 4A       | LSR                        |
| CFB4 4D 00 DD | EOR \$DD00 2 Bit           |
| CFB7 49 30    | EOR #\$30                  |
| CFB9 C8       | INY                        |
| CFBA 4A       | LSR                        |
| CFBB 4A       | LSR                        |
| CFBC 4D 00 DD | EOR \$DD00 2 Bit           |
| CFBF 4A       | LSR                        |
| CFC0 4A       | LSR                        |
| CFC1 4D 00 DD | EOR \$DD00 2 Bit           |
| CFC4 91 FD    | STA (\$FD), Y Byte ablegen |
| CFC6 EA       | NOP                        |
| CFC7 AD 00 DD | LDA \$DD00 2 Bit           |
| CFC8 4A       | LSR                        |
| CFC9 4A       | LSR                        |
| CFCB 4D 00 DD | EOR \$DD00 2 Bit           |
| CFCF 49 30    | EOR #\$30                  |
| CFD1 C8       | INY                        |

1. Byte

2. Byte

3. Byte

4. Byte

|               |                                   |
|---------------|-----------------------------------|
| CFD2 4A       | LSR                               |
| CFD3 4A       | LSR                               |
| CFD4 4D 00 DD | EOR \$DD00 2 Bit holen            |
| CFD7 4A       | LSR                               |
| CFD8 4A       | LSR                               |
| CFD9 4D 00 DD | EOR \$DD00 2 Bit holen            |
| CFDC 91 FD    | STA (\$FD), Y Byte ablegen        |
| CFDE CA       | DEX                               |
| CFDF D0 88    | BNE \$CF69 2x                     |
| CFE1 A9 23    | LDA #\$23                         |
| CFE3 BD 00 DD | STA \$DD00                        |
| CFE6 C0 FF    | CPY \$FF                          |
| CFE8 F0 03    | BEQ \$CFED                        |
| CFEA 4C 5A CF | JMP \$CF5A                        |
| CFED C8       | INY = 0                           |
| CFEE A5 FF    | LDA \$FF                          |
| CFF0 C8       | INY                               |
| CFF1 C8       | INY                               |
| CFF2 51 FD    | EOR (\$FD), Y                     |
| CFF4 C8       | INY                               |
| CFF5 51 FD    | EOR (\$FD), Y                     |
| CFF7 C8       | INY                               |
| CFF8 D0 F6    | BNE \$CFF0                        |
| CFFA 20 8A CE | JSR \$CEBA Byte laden (Prüfsumme) |
| CFFD 4C FC DE | JMP \$CEEC Byte holen             |

176 Zyklen für 4 Bytes

Sektorende

Prüfsumme